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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/679,873	10/06/2003	Robert Everest Johnson	ANCO / 89US	3837
26875	7590	05/04/2005	EXAMINER	
WOOD, HERRON & EVANS, LLP			CHOE, HENRY	
2700 CAREW TOWER				
441 VINE STREET			ART UNIT	
CINCINNATI, OH 45202			PAPER NUMBER	
			2817	

DATE MAILED: 05/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/679,873

Applicant(s)

JOHNSON ET AL.

Examiner

Henry K. Choe

Art Unit

2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 October 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-53 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 29 is/are allowed.
- 6) ☒ Claim(s) 1, 2, 15, 16, 30-33 and 42-48 is/are rejected.
- 7) ☒ Claim(s) 3-14, 17-28, 34-41 and 49-53 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 06 October 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>1/13/05; 3/8/04; 3/2/04</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 2, 15, 16, 30-33 and 42-48 are rejected under 35 U.S.C. 102(b) as being anticipated by Jeckeln et al (Figs. 1 and 4).

Regarding claims 1, 15, 30-33, 42, 47 and 48, Jeckeln et al (Figs. 1 and 4) discloses an amplifier circuit comprising an RF power amplifier (13), a predistorter (2) which is coupled to the RF power amplifier (13) and includes a vector modulator (19), a data structure (3, 4) which is coupled to the vector modulator (19) and storing the correction factor and wherein the correction factors compensate the amplitude and phase non-linearities of the RF power amplifier (13) and the vector modulator [(19) which is well known functions of the correction factors in the predistortion amplifier art].

Regarding claims 2 and 16, Jeckeln et al (Figs. 1 and 4) discloses an amplifier circuit further comprising a first pair of look-up tables (3, 4).

Regarding claims 43-46, the look-up tables (3 and 4) in Fig. 4 of Jeckeln et al are functionally equivalent to the claimed limitations.

Claims 1, 2, 15, 16, 30-33 and 42-48 are rejected under 35 U.S.C. 102(b) as being anticipated by Wright et al (Figs. 1 and 3).

Regarding claims 1, 15, 30-33, 42, 47 and 48, Wright et al (Figs. 1 and 3) discloses an amplifier circuit comprising an RF power amplifier (60), a predistorter (52) which is coupled to the RF power amplifier (60) and includes a vector modulator (52B), a data structure (52H) which is coupled to the vector modulator (52B) and storing the correction factor and wherein the correction factors compensate the amplitude and phase non-linearities of the RF power amplifier (60) and the vector modulator [(52B) which is well known functions of the correction factors in the predistortion amplifier art].

Regarding claims 2 and 16, Wright et al (Figs. 1 and 3) discloses an amplifier circuit further comprising a first pair of look-up tables (52H).

Regarding claims 43-46, the look-up tables (52H) in Fig. 3 of Wright et al are functionally equivalent to the claimed limitations.

Allowable Subject Matter

Claims 3-14, 17-28, 34-41 and 49-53 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Reasons for Allowance

Claim 29 is allowed.

The following is a statement of reasons for the indication of allowable subject matter: Regarding claim 29, the closest prior art of record, Jeckin et al (Figs. 1 and 4)

Art Unit: 2817


does not disclose the following limitations: the internal structures of the first and second pairs of look up tables.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (6,246,286; 6,275,685; 6,462,617) are the amplifiers with the predistorters.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-1760.


HENRY CHOE
PRIMARY EXAMINER

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